

Filter example project

1.10

Features

- FIR Low Pass filter with Rectangular window
- 128 Filter taps
- 1 kHz Cutoff Frequency
- DMA request is used as the Data ready signal

General Description

This example project demonstrates the operation of a Low Pass FIR Filter with Rectangular window.

Development kit configuration

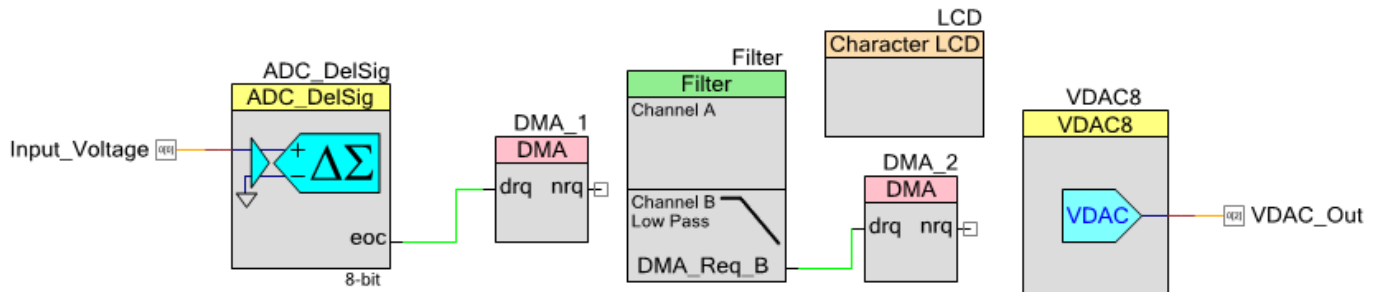
1. This project is written for a 2X16 LCD display as there is one available in the Cypress kit CY8CKIT-001.
2. Build the project and program the hex file on to CY8C3866AXI-040 using MiniProg3.
3. Connect pins as described below and power cycle the device.
4. Observe the ADC output and also the filter input and output register values on LCD.
5. Observe the VDAC output as described below.

Project configuration

The example project consists of the Filter, ADC_DeISig, DMA, VDAC and Char LCD components. The top design schematic is shown in Figure 1. The Character LCD component is used for displaying the ADC output and also the filter input and output register values.

Test Setup:

- 1) Positive terminal of ADC is connected to the analog pin which is mapped to P0[0] of CY8CKIT-001. Connect sine wave input with frequency below 1kHz (cutoff frequency) and amplitude below 1V with the voltage staying in the range between 0V and 1.024V.
- 2) ADC output is connected to drq input of DMA component so that on end of each successful conversion, the DMA transfers ADC output from ADC output register to Filter input staging register.
- 3) A low pass filter with rectangular window is used to filter out the ADC output. After completion of filtering of each input sample, a level signal will be generated. This signal is used as data request for DMA_2. That is DMA transfers filtered output to VDAC data register.
- 4) VDAC converts the filtered output to analog signal. VDAC output is connected to analog pin which is mapped to P0[2] of CY8CKIT-001. Observe the VDAC output using an oscilloscope.
- 5) LCD is used to print the ADC output value and the value of Filter input and output registers. LCD is mapped to P2[6:0] of CY8CKIT-001.

**Procedure :**

1. Build the project and program the hex file on to the target device.
2. Power cycle the device and observe the results on the LCD.
3. The ADC output is displayed on the LCD module which corresponds to resultant input analog value given to input terminal of ADC. LCD will also display Filter input and output register values.
4. Observe the VDAC output from port P0[2] using an oscilloscope.

Figure 1. Top design schematic.

The Character LCD and VDAC uses the default configuration. The ADC is configured in the default single ended mode with Continuous conversion mode for 8-bit resolution. The DMA is configured to have the Level triggered hardware request signal. The Filter component is configured as shown below in figure 2.

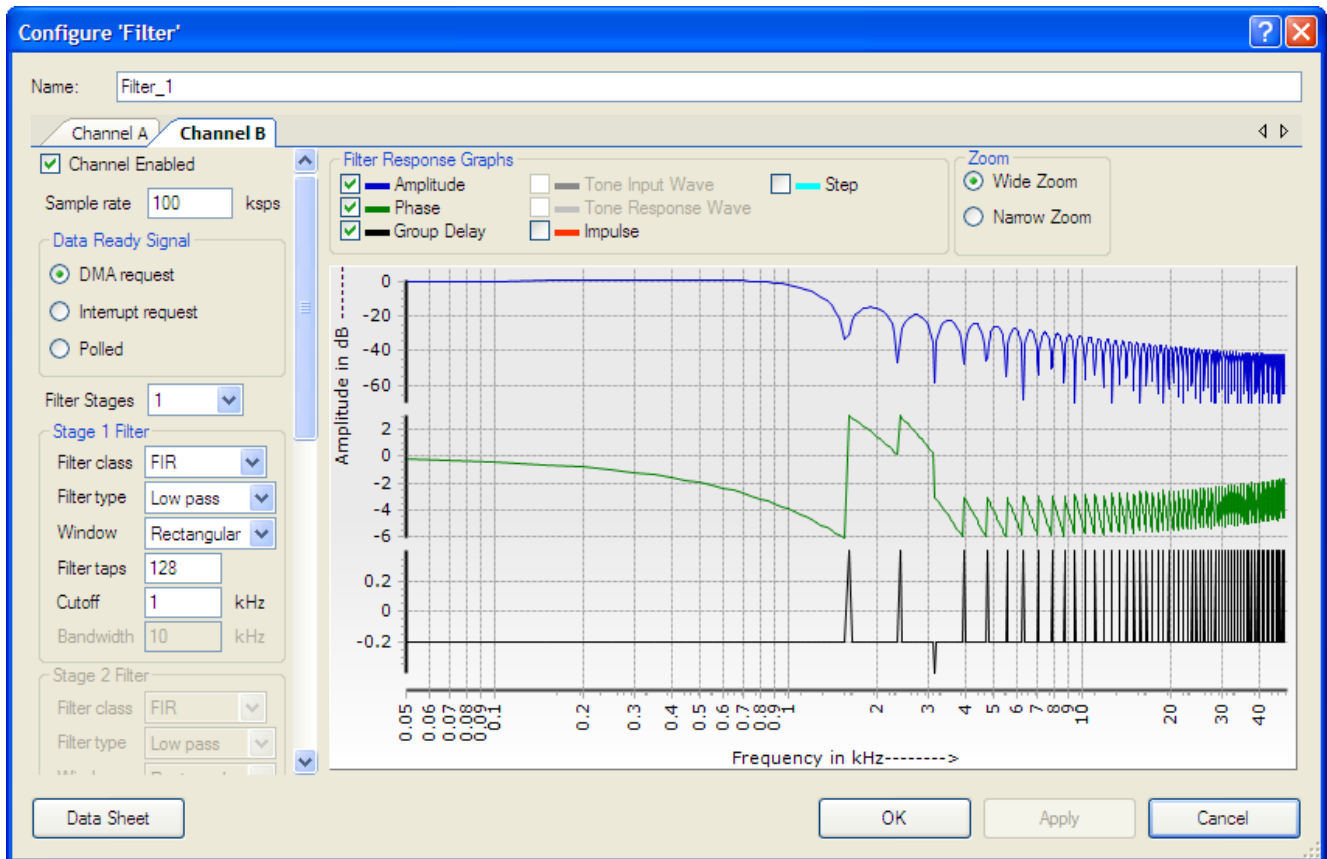


Figure 2. Filter Component Configuration.

Project description

In the main function all components are started. For the proper usage of Character LCD, VDAC, ADC_DelSig components, please refer to the corresponding component datasheets.

In this project, ADC output is given as the input sample to the low pass FIR filter. Connections are made as explained in the Figure 1 above. ADC component converts input analog voltage given to its +ve terminal. On each successful conversion, DMA_1 transfers the ADC output to Filter input register. Filtered output is stored in the Filter output register. Filter generates data ready signal on filtering each of the input samples. This data ready signal is used to transfer the filtered output to VDAC data register using DMA_2. LCD is used in this project to display the ADC output and Filter input and output register values.

Expected Results

The sine wave observed on the output terminal of the VDAC should be equal in amplitude and frequency to the sine wave given to the input terminal of ADC component.

© Cypress Semiconductor Corporation, 2009-2011. The information contained herein is subject to change without notice. Cypress Semiconductor Corporation assumes no responsibility for the use of any circuitry other than circuitry embodied in a Cypress product. Nor does it convey or imply any license under patent or other rights. Cypress products are not warranted nor intended to be used for medical, life support, life saving, critical control or safety applications, unless pursuant to an express written agreement with Cypress. Furthermore, Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress products in life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

PSoC® is a registered trademark, and PSoC Creator™ and Programmable System-on-Chip™ are trademarks of Cypress Semiconductor Corp. All other trademarks or registered trademarks referenced herein are property of the respective corporations.

Any Source Code (software and/or firmware) is owned by Cypress Semiconductor Corporation (Cypress) and is protected by and subject to worldwide patent protection (United States and foreign), United States copyright laws and international treaty provisions. Cypress hereby grants to licensee a personal, non-exclusive, non-transferable license to copy, use, modify, create derivative works of, and compile the Cypress Source Code and derivative works for the sole purpose of creating custom software and or firmware in support of licensee product to be used only in conjunction with a Cypress integrated circuit as specified in the applicable agreement. Any reproduction, modification, translation, compilation, or representation of this Source Code except as specified above is prohibited without the express written permission of Cypress.

Disclaimer: CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS MATERIAL, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Cypress reserves the right to make changes without further notice to the materials described herein. Cypress does not assume any liability arising out of the application or use of any product or circuit described herein. Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress' product in a life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

Use may be limited by and subject to the applicable Cypress software license agreement.